

Do NOT Enter

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In the Claims

1. (Currently Amended) A switch circuit comprising:
a first circuit portion corresponding to a first input port;
a second circuit portion corresponding to a second input port; and
an output port,
wherein each of the first and second circuit portions include at least one first transistor providing a portion of an isolation channel, at least one second transistor providing a portion of a transmit channel, and at least two third transistors for providing a control bias for selecting either the transmit channel or the isolation channel; and
wherein each third transistor of the first circuit portion is coupled at its base to a base of a corresponding third transistor of the second circuit portion, and to a control voltage source.
2. (Original) The switch circuit of claim 1, wherein the circuit is formed as an integrated circuit.
3. (Currently Amended) The switch circuit of claim 1, wherein the at least two third transistors of each of the first and second circuit portions provides a control bias for selecting which of the first and second input ports are coupled to the output port.
4. (Original) The switch circuit of claim 1, wherein the at least one first transistor comprises two transistors and the at least one second transistor comprises two transistors.